

TRANSMITTAL FOR U.S. PATENT APPLICATION
UNDER 37 CFR §1.53(b)

22388 U.S.PTO
10/687796
101703


Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attorney Docket No. 032350.B519 (TI-35921)
Express Mail Certificate
No. EV 324626605 US

Sir:

Transmitted herewith for filing is the original patent application of:

Inventor: Vijay Kumar Reddy, et al.

Filing Date: October 17, 2003

For: **METHOD AND SYSTEM FOR DETERMINING TRANSISTOR
DEGRADATION MECHANISMS**

Enclosed are:

- Specification, Claims and Abstract (21 Pages);
- Drawing(s) 2 Total Sheet(s) of X Formal Informal;
- Combined Declaration and Power of Attorney (1 Page)
 Newly executed (original or copy);
- An Assignment (1 Page) of the invention to Texas Instruments Incorporated is attached. A separate cover sheet (1 Page) in compliance with 37 C.F.R. §§ 3.28, .31 is included. Please charge the assignment recordal fee of \$40.00 pursuant to 37 C.F.R. § 1.21(h) to Deposit Account No. 20-0668 of Texas Instruments Incorporated;
- Certificate of Mailing (1 Page);
- Texas Instrument Incorporated Return Receipt Postcard (1);
- Baker Botts L.L.P. Return Receipt Postcard (1);
- This Transmittal (2 pages); and
- Information Disclosure Statement
with PTO 1449

Applicant is:

Large Entity
 Small Entity

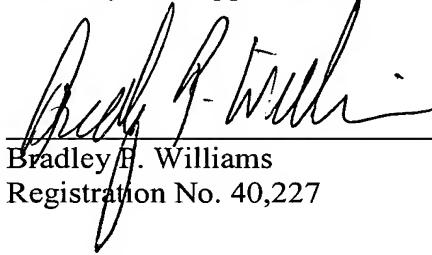
The accompanying application is:

Original.
 Continuation Divisional Continuation-In-Part (CIP) of prior application No.
which is hereby incorporated by reference therein.

FEE CALCULATION					FEE
	Number		Number Extra	Rate	Basic Fee
Total Claims	20	- 20	-0-	X \$18.00 =	\$ 0.00
Independent Claims	3	-3	-0-	X \$84.00 =	0.00
TOTAL FILING FEE =					\$ 770.00

Please charge the filing fee of **\$770.00** and any other fee required by this paper and credit any overpayment to Deposit Account No. 20-0668 of TEXAS INSTRUMENTS INCORPORATED. **This document is being transmitted in duplicate.**

BAKER BOTTS L.L.P.
Attorneys for Applicants



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Date: October 17, 2003

Correspondence Address:

Customer Number: **23494**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Vijay Kumar Reddy, et al
Filing Date: October 17, 2003
Title: **METHOD AND SYSTEM FOR DETERMINING
TRANSISTOR DEGRADATION MECHANISMS**

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that the attached Filing Fee Transmittal (2 pages – in duplicate); Patent Application (21 pages) including Specification, 20 Claims, and Abstract; Formal Drawings (2 Sheets); executed Declaration and Power of Attorney (1 page); executed Assignment (1 page); Assignment Recordation Coversheet (1 page – in duplicate); Baker Botts L.L.P. return receipt postcard (1 card); Texas Instruments Incorporated return receipt postcard (1 card); and this Certificate of Mailing are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on this 17th day of October, 2003 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Willie Jiles

Willie Jiles

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